



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shinji Maekawa Art Unit : 2813  
Serial No.: 09/724,403 Examiner : Yennhu B. Huynh  
Filed : November 27, 2000  
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE  
SAME

Commissioner for Patents  
Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Official Action mailed January 29, 2001,  
Applicant elects the invention of Species I: claims 1-32, drawn  
to a method of manufacturing a semiconductor device. The  
election is made without traverse.

AMENDMENT

Kindly amend the above-referenced application as follows.

In the Claims:

Please amend the claims as follows.

1. (Amended) A method of manufacturing a semiconductor

04/15/2002 66EBRE61 00000025 09724403

01 FC:103  
02 FC:115

288.00 OP  
110.00 OP

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being  
deposited with the United States Postal Service as first class mail with  
sufficient postage on the date indicated below and is addressed to the  
Commissioner for Patents, Washington, D.C. 20231.

March 28, 2002

Date of Deposit

Signature

Susan Regan

Typed or Printed Name of Person Signing Certificate

#4/a

cl. 1-32

E.O.T  
(M)  
JL

RECEIVED  
APR 16 2002  
TECHNOLOGY CENTER 2800

Q1 SUB C17 / C